

REMARKS

Favorable reconsideration of this application is requested in view of the following remarks. For the reasons set forth below, Applicant respectfully submits that the claimed invention is allowable over the cited reference.

The Office Action dated January 30, 2003, indicated that claims 1, 4, 6, 8, 9, 12, 15 and 17 stand rejected under §102(b) as being anticipated by *Farrow*. (U.S. Patent No. 5,793,243); and claims 2, 3, 5, 7, 10, 11, 13, 14 and 16 are objected to as being dependent upon a rejected base claim but would be allowable if rewritten.

Applicant appreciates the indication of allowability of claims 2, 3, 5, 7, 10, 11, 13, 14 and 16. Claims 2, 5, 7, 10, and 13 have been amended to include all limitations of their respective base and intervening claims and since claims 3 and 16 depend from claim 2, claim 11 depends from claim 10, and claim 14 depends from claim 13, Applicant submits that each of the objected-to claims are in condition for allowance.


Applicant respectfully traverses the §102(b) rejection of the claims as the Office Action fails to present a reference that teaches or suggests all aspects of the claimed invention. First, the Office Action fails even to assert correspondence to the limitations of Applicant's claim 1 regarding "an output for delivering an output signal to an output of the circuit." The cited '243 reference's signal 82 of Fig. 3 fails to correspond as it is an internal circuit signal and does not exit the circuit. Second, the Office Action fails to present correspondence to Applicant's claimed "means for reducing the DC-offset and the noise produces by the amplifier" as required by claim 1. The allegedly corresponding elements 62, 69 and 72 are contained within circuit 60 which "is used to extract the equivalent DC voltage of stabilization amplifier 26" and not directed to Applicant's claim limitations. Col. 6, lines 1-3. Third, the Office Action fails to present correspondence to the claim limitations regarding "feedback means for further reducing the DC-offset produced by the amplifier." The cited portions of the '243 reference, switching means 92 and adder circuit 88, merely switch above-discussed circuit 60 from a stabilization mode to an integration mode without being directed to further reducing any DC-offset. The Office Action's failure to provide or even assert correspondence to each limitation of the instant invention requires that the rejection fail and Applicant requests that the rejection be withdrawn.

Please charge Deposit Account No. 50-0996 (PENA.022C1) in the amount of \$420.00 for the five new independent claims and charge/credit the same Deposit Account No. for any deficiency/surplus.

In view of the remarks above, Applicant believes that each of the rejections has been overcome and the application is in condition for allowance. Should there be any remaining issues that could be readily addressed over the telephone, the Examiner is asked to contact the agent overseeing the application file, Mr. Eric M. Bram, of Philips Corporation at (914) 333-9635.

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